IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: T. Lakshmi Viswanathan, et al.   Art Unit: 2816

Serial No.: 10/749,275   Examiner: Jeffery S. Zweizig

Filed: 12/29/03   Docket: TI-36954

For: LOAD SENSING VOLTAGE REGULATOR FOR PLL/DLL ARCHITECTURES

Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

AMENDMENT

In response to the Official Action in this case mailed January 7, 2005, please enter the following:

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In re the Application of

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Karen Vertz   4-7-05
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____ DIVISIONAL APP'N	

NAME OF INVENTOR(S):	RECEIPT DATE & SERIAL NO.:
T. Lakshmi Viswanathan, et al.	Serial No.: 10/749,275
TITLE OF INVENTION: Load Sensing Voltage Regulator for PLL/DLL Architectures	Filing Date: 12/29/03

TI FILE NO.:	DEPOSIT ACCT. NO.:
TI-36954	20-0668

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